

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): B. Arlen Young

Assignee: Adaptec, Inc.

Title: A METHOD FOR CONFIGURING A SINGLE HARDWARE I/O
CONTROL BLOCK ARCHITECTURE FOR USE WITH BOTH
MIRRORED AND NON-MIRRORED DATA TRANSFERS (As
Amended)

Serial No.: Unknown Filed: Herewith

Divisional of Serial No. 10/051,907 Filed January 16, 2002

Examiner: Tanh Q. Nguyen Group Art Unit 2182

Docket No.: ADPT105103

Monterey, CA
February 12, 2004

Mail Stop Patent Application
Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

After the above-identified divisional application receives a serial number, please amend the above-identified divisional application as follows:

1. **Amendments to the Specification** begin on page 2 of this paper;
2. **Amendments to the Claims** are reflected in the listing of Claims which begins on page 3 of this paper; and
3. **Remarks** begin on page 6 of this paper.